## COMBINED DECLARATION AND POWER OF ATTORNEY

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below by my name;

I believe I am the original, first and sole inventor, if only one name is listed below, or an original, first and joint inventor if multiple names are listed below, of the subject matter which is claimed and for which a patent is sought on the invention entitled:

## INTEGRATED CIRCUIT ASSEMBLY MODULE THAT SUPPORTS CAPACITIVE COMMUNICATION BETWEEN SEMICONDUCTOR DIES

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FILED PRIOR TO THE FILING DA	
(Day, Month, Year)	PRIORITY CLAIMED
	YES NO
le, §119(e), of any United States p	provisional application(s) listed belo
DATE OF FILING	
29 January 2003	
1	DAT

APPLICATION NUMBER	DATE OF FILING	STATUS		
		PATENTED	PENDING	ABANDONED

of the prior application and the national or PCT international filing date of this application:

material to patentability as defined in Title 37, Code of Federal Regulations, §1.56, which became available between the filing date

## Attorney Docket No. SUN-P9610-SPL

41,752) and A. Richard Park (Reg. No. 41,241) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith, and to file, prosecute and transact all business in connection with international applications directed to said invention.

Address correspondence to:

Direct telephone calls to:

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A. Richard Park

PATENT TRADEMARK OFFICE (530) 759-1661

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, §1001, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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		Additional inventor name(s) and significant	gnature(s) attacl	ned?: YES □ NO ☒	